

First Named Inventor	Agostino A. Macerola
Serial No.	Filed Herewith
Filing Date	Filed Herewith
Group Art Unit	2824
Examiner Name	Unknown
Confirmation No.	Unknown
Attorney Docket No.	400.238US01
Title: HIGH VOLTAGE GENERATION AND REGULATION CIRCUIT IN A MEMORY DEVICE	

**INFORMATION
DISCLOSURE
STATEMENT**

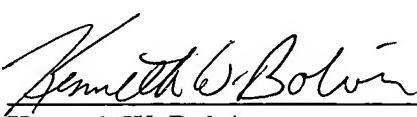
Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Pursuant to 37 C.F.R. 1.98 (a)(2) (i), as this application was filed after June 30, 2003, Applicant has not included copies of U.S. Patents or U.S. Patent Applications. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

As an Office Action has not yet issued in this application, Applicant believes that no fees are due. However, the Commission for Patents is hereby authorized to charge any additional fees to Deposit Account No. 501373. If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Respectfully submitted,

Date: 3/26/04



 Kenneth W. Bolvin
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First Named Inventor	Agostino A. Macerola	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
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Title: HIGH VOLTAGE GENERATION AND REGULATION CIRCUIT IN A MEMORY DEVICE		

Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
	4,389,705	06/21/1983	Sheppard	08/21/1981
	6,219,293	04/17/2001	Butler et al.	09/01/1999
	6,480,436	11/12/2002	Confalonieri , et al.	07/19/2001
	6,545,930	04/08/2003	Fischer, et al.	11/13/2001
	6,697,288	02/24/2004	Kim, et al.	12/28/2001
	6,707,715	03/16/2004	Michael et al.	08/02/2001

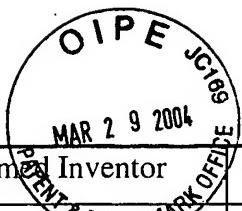
Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			

Other References					
Examiner Initials	Author, Title, Date, Pages, etc.				

Examiner Signature		Date Considered
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce



First Name & Inventor	Agostino Macerola
Serial No.	10/726,265
Filing Date	December 2, 2003
Group Art Unit	2824
Examiner Name	Unknown
Confirmation No.	3393
Attorney Docket No.	400.238US01

COMMUNICATION RE:
FILING OF PRIORITY
DOCUMENT
UNDER 35 USC 119

Title: HIGH VOLTAGE GENERATION AND REGULATION CIRCUIT IN A MEMORY DEVICE

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In accordance with the requirements for claiming right of priority under 35 U.S.C. 119, enclosed for filing in the above-identified application is a certified copy of Applicant's priority application RM 2003A000338 (Italy) as filed on 11 July 2003.

Please contact the undersigned attorney at (612) 312-2200 if you have any questions.

Respectfully submitted,

Date: 3/26/04


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